

**PATENT APPLICATION**

Sheet 1 of 7

<b>FORM PTO-1449</b>  <b>LIST OF PATENTS AND PUBLICATIONS FOR APPLICANT'S INFORMATION DISCLOSURE STATEMENT</b>  (Use several sheets if necessary)	<b>ATTY. DOCKET NO.</b> 200310172-1	<b>APPLICATION NO.</b> 10/768,442	<b>CONFIRMATION NO.</b>  
<b>APPLICANT</b> S. Brandon Keller, et al.			
<b>FILING DATE</b> Herewith 1/30/4		<b>GROUP</b> 2825	

**REFERENCE DESIGNATION**

**U.S. PATENT DOCUMENTS**

EXAMINER INITIAL	DOCUMENT NUMBER	PUBLICATION DATE	NAME	Pages, Columns, Lines Where Relevant Passages or Figures Appear
	1A			
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**FOREIGN PATENT DOCUMENTS**

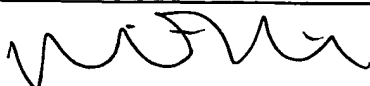
DOCUMENT NUMBER	PUBLICATION DATE	NAME OF PATENTEE OR APPLICANT	Pages/Columns/Lines Where Relevant Passages/Figures Appear	Check if Translation attached
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**OTHER REFERENCES (including Author, Title, Date, Pertinent Pages, etc.)**

W	1Q	KELLER, S. Brandon; ROGERS, Dennis R.; ROBBERT, George H.; U.S. Patent Application Serial Number 10/647,596, Entitled: System And Method Analyzing Design Elements In Computer Aided Design Tools; Filed August 25, 2003
M	1R	KELLER, S. Brandon; ROGERS, Dennis R.; ROBBERT, George H.; U.S. Patent Application Serial Number 10/647,608, Entitled: System And Method For Determining Unmatched Design Elements In A Computer-Automated Design; Filed August 25, 2003
M	1S	KELLER, S. Brandon; ROGERS, Dennis R.; ROBBERT, George H.; U.S. Patent Application Serial Number 10/647,606; Entitled: System And Method For Determining Connectivity Of Nets In A Hierarchical Circuit Design; Filed August 25, 2003

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	2Q	KELLER, S. Brandon; ROGERS, Dennis R.; ROBBERT, George H.; U.S. Patent Application Serial Number 10/647,598, Entitled: Computer Aided Design System And Methods With Reduced Memory Utilization; Filed August 25, 2003
	2R	KELLER, S. Brandon; ROGERS, Dennis R.; ROBBERT, George H.; U.S. Patent Application Serial Number 10/647,688, Entitled: System And Method For Iteratively Traversing A Hierarchical Circuit Design; Filed August 25, 2003
	2S	KELLER, S. Brandon; ROGERS, Dennis R.; ROBBERT, George H.; U.S. Patent Application Serial Number 10/647,789, Entitled: Systems And Methods For Establishing Data Model Consistency Of Computer Aided Design Tools; Filed August 25, 2003

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W	3Q	KELLER, S. Brandon; ROGERS, Dennis R.; ROBBERT, George H.; U.S. Patent Application Serial Number 10/647,594, Entitled: Systems And Methods For Determining Activity Factors Of A Circuit Design; Filed August 25, 2003
W	3R	KELLER, S. Brandon; ROGERS, Dennis R.; ROBBERT, George H.; U.S. Patent Application Serial Number 10/647,687, Entitled: Systems And Methods Utilizing Fast Analysis Information During Detailed Analysis Of A Circuit Design; Filed August 25, 2003
W	3S	KELLER, S. Brandon; ROGERS, Dennis R.; ROBBERT, George H.; U.S. Patent Application Serial Number 10/647,595, Entitled: System And Method For Determining Applicable Configuration Information For Use In Analysis Of A Computer Aided Design; Filed August 25, 2003

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W	4Q	KELLER, S. Brandon; ROGERS, Dennis R.; ROBBERT, George H.; U.S. Patent Application Serial Number 10/647,607, Entitled: Systems And Methods For Identifying Data Sources Associated With A Circuit Design; Filed August 25, 2003
W	4R	KELLER, S. Brandon; ROGERS, Dennis R.; ROBBERT, George H.; U.S. Patent Application Serial Number 10/647,605, Entitled: Systems And Methods For Performing Circuit Analysis On A Circuit Design; Filed August 25, 2003
W	4S	KELLER, S. Brandon; ROGERS, Dennis R.; ROBBERT, George H.; U.S. Patent Application Serial Number 10/647,768, Entitled: System And Method For Determining A Highest Level Signal Name In A Hierarchical VLSI Design; Filed August 25, 2003

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W	5Q	KELLER, S. Brandon; ROGERS, Dennis R.; ROBERT, George H.; U.S. Patent Application Serial Number 10/847,597, Entitled: System And Method For Determining Wire Capacitance For A VLSI Circuit; Filed August 25, 2003
M-	5R	KELLER, S. Brandon; ROGERS, Dennis R.; LELM, Charles A.; U.S. Patent Application Serial Number 10/708,682, Entitled: Method And Program Product For Determining Nets Requiring Detailed Electromigration And Self Heating Analysis In A Digital Integrated Circuit; Filed November 12, 2003
W	5S	KELLER, S. Brandon; ROGERS, Dennis R.; LELM, Charles A.; U.S. Patent Application Serial Number 10/708,698, Entitled: Method And Program Product For Performing Self-Heating Analysis In A Digital Integrated Circuit Through A Single Cycle Transient Simulation; Filed November 12, 2003

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## OTHER REFERENCES (including Author, Title, Date, Pertinent Pages, etc.)

W	6Q	KELLER, S. Brandon; ROGERS, Dennis R.; LELM, Charles A.; U.S. Patent Application Serial Number 10/706,376, Entitled: Method And Program For Visual Display and One-Click Repair Of Electromigration And Self Heating Design-Rule Violations In A Digital Integrated Circuit Layout Database; Filed November 12, 2003
W	6R	KELLER, S. Brandon; ROGERS, Dennis R.; LELM, Charles A.; U.S. Patent Application Serial Number 10/706,501, Entitled: Method And Program Product For Performing Electromigration Analysis In A Digital Integrated Circuit By Converting A Netlist To A DC Model And Performing DC Analysis Of The DC Model; Filed November 12, 2003
W	6S	KELLER, S. Brandon; ROGERS, Dennis R.; LELM, Charles A.; U.S. Patent Application Serial Number 10/706,526, Entitled: Method And Program Product For Performing A Worst Case Electromigration And Self Heating Analysis In A Digital Integrated Circuit With Worst-Case Superposition Of Partial Currents; Filed November 12, 2003

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<i>M</i>	7Q	KELLER, S. Brandon; ROGERS, Dennis R.; LELM, Charles A.; U.S. Patent Application Serial Number 10/706,692, Entitled: Method And Program Product For Performing Electromigration Analysis In A Digital Integrated Circuit Through A Single Cycle Transient Simulation; Filed November 12, 2003
<i>W</i>	7R	KELLER, S. Brandon; ROGERS, Dennis R.; LELM, Charles A.; U.S. Patent Application Serial Number 10/706,508, Entitled: Method And Program Product For Performing Self-Heating Analysis In A Digital Integrated Circuit Layout Database by Substituting Resistive Models For Active Devices; Filed November 12, 2003
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